

## GATE 2019 Result

Name

SAGAR KADEMANI

Registration Number

EE19S61225066

Gender

Male

Examination Paper

Electrical Engineering (EE)



Marks out of 100<sup>#</sup>

21.33

Qualifying Marks<sup>##</sup>

39.6

General

35.6

OBC (NCL)

26.4

SC/ST/PwD

<sup>#</sup> Normalized marks for multisession papers (CE and ME)

<sup>##</sup> A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category for which a valid Category Certificate, if applicable, is produced along with this scorecard.

### Note:

- The marks provided here are for information only.
- An electronic or paper copy of this document is not valid for admission.
- The official GATE 2019 Score Card can be downloaded from the GOAPS site between March 20, 2019 and May 31, 2019 by the qualified candidates only.
- For the papers CE and ME, qualifying marks and score are based on "Normalized Marks".



Dr. M H SIDRAM B.E., M.Tech.(IISc), Ph.D  
Associate Professor and Head  
Department of Electrical and Electronics Engineering  
Sri Jayachamarajendra College of Engineering  
JSS Science and Technology University, Mysuru-570 006